

<b>L Number</b>	<b>Hits</b>	<b>Search Text</b>	<b>DB</b>	<b>Time stamp</b>
<b>1</b>	<b>732</b>	<b>"clock generator" and "quality" and "synchronous" and "clock signal" and @ad&lt;19980828</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/21 13:23</b>
<b>2</b>	<b>2</b>	<b>"clock generator" and "quality" and "synchronous" and "two clock signal" and @ad&lt;19980828 .</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/21 13:23</b>
<b>3</b>	<b>45</b>	<b>"clock generator" and "quality" and "synchronous" and "two clock signals" and @ad&lt;19980828</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/21 13:56</b>
<b>5</b>	<b>5593</b>	<b>((370/395) or (370/503) or (370/507) or (370/516) or (370/108) or (375/356) or (375/357) or (375/362) or (327/7)).CCLS.</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/21 13:22</b>
<b>8</b>	<b>32</b>	<b>((((370/395) or (370/503) or (370/507) or (370/516) or (370/108) or (375/356) or (375/357) or (375/362) or (327/7)).CCLS.) and "clock generator" and "quality" and "synchronous" and "clock signal" and @ad&lt;19980828</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/21 13:23</b>
<b>9</b>	<b>1</b>	<b>((((370/395) or (370/503) or (370/507) or (370/516) or (370/108) or (375/356) or (375/357) or (375/362) or (327/7)).CCLS.) and "clock generator" and "quality" and "synchronous" and "two clock signal" and @ad&lt;19980828</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/21 13:23</b>
<b>10</b>	<b>4</b>	<b>((((370/395) or (370/503) or (370/507) or (370/516) or (370/108) or (375/356) or (375/357) or (375/362) or (327/7)).CCLS.) and "clock generator" and "quality" and "synchronous" and "two clock signals" and @ad&lt;19980828</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/21 13:23</b>
<b>11</b>	<b>0</b>	<b>"clock generator" and "quality" and "synchronous" and "two clock signals" and "interface card" and @ad&lt;19980828</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/21 13:57</b>
<b>12</b>	<b>0</b>	<b>((((370/395) or (370/503) or (370/507) or (370/516) or (370/108) or (375/356) or (375/357) or (375/362) or (327/7)).CCLS.) and "clock generator" and "quality" and "synchronous" and "two clock signals" and "interface card" and @ad&lt;19980828</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2004/10/21 13:57</b>

14	36	"clock generator" and "quality" and "synchronous" and "two clock signals" and "interface" and @ad<19980828	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 13:57
13	2	((((370/395) or (370/503) or (370/507) or (370/516) or (370/108) or (375/356) or (375/357) or (375/362) or (327/7)).CCLS.) and "clock generator" and "quality" and "synchronous" and "two clock signals" and "interface" and @ad<19980828	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 14:15
15	0	((((370/395) or (370/503) or (370/507) or (370/516) or (370/108) or (375/356) or (375/357) or (375/362) or (327/7)).CCLS.) and "clock generator" and "quality" and "synchronous" and "two clock signals" and "interface" and "peripheral processor" and @ad<19980828	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 14:16
16	1	((((370/395) or (370/503) or (370/507) or (370/516) or (370/108) or (375/356) or (375/357) or (375/362) or (327/7)).CCLS.) and "clock generator" and "quality" and "synchronous" and "two clock signals" and "interface" and "processor" and @ad<19980828	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 14:16